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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/803,792	03/17/2004	Nam-Jung Her	4591-363	5596

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EXAMINER	
RADOSEVICH, STEVEN D	
ART UNIT	PAPER NUMBER
2117	
MAIL DATE	DELIVERY MODE
11/27/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Supplemental Notice of Allowability	Application No. 10/803,792	Applicant(s) HER ET AL.	
	Examiner Steven D. Radosevich	Art Unit 2117	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *9/4/07*.

2. ☒ The allowed claim(s) is/are *1-4,7-11,16-18 and 20*.

3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____ .
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

 * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

 Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date _____ .
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

SHELLY CHASE
PRIMARY EXAMINER

DETAILED ACTION

In response to the interview on 10/10/2007, this supplemental action identifies claim 17 as being included within the claims being allowed and also includes page 4, which was not mailed to the applicant within the prior notice of allowance mailed 9/4/07.

Priority

Acknowledgment is made that foreign priority is claimed for this application and as such the data, 3/17/2003, is being used for this examination.

Drawings

Acknowledgment is made that the figures are accepted at this time since no further issues are found at this time that would require an objection and/or correction.

Allowable Subject Matter

The following is an examiner's statement of reasons for allowance:

Claims 1-4, 7-11, 16-18, and 20 are allowable.

The present invention pertains to selectively providing output signals in a test mode and normal mode, wherein in the test mode that output signals are provided in a reduced output pin requirement configuration. The claims invention recites features such as: "a plurality of data output pins divided in at least two groups of data output pins; a data processing circuit to generate output signals responsive to an input signal; and an output selection circuit with at least a normal mode and a test mode; where a first group of output signals are provided to a first group of data output pins during a first test cycle of the test mode; where a second group of output signals are provided to the first group of data output pins during a second test cycle of the test mode; and where

during the normal mode: the first group of output signals are provided to the corresponding data output pins of the first group of data output pins; and the second group of output signals are provided to the corresponding data output pins of a second group of data output pins."

None of the prior art, either taken by itself or in any combination with any other prior art(s), would have anticipated or made obvious the following combination of limitations within the above limitations at of before the time the invention within this application was filed: "a plurality of data output pins divided in at least two groups of data output pins; a data processing circuit to generate output signals responsive to an input signal; and an output selection circuit with at least a normal mode and a test mode; where a first group of output signals are provided to a first group of data output pins during a first test cycle of the test mode; where a second group of output signals are provided to the first group of data output pins during a second test cycle of the test mode; and where during the normal mode: the first group of output signals are provided to the corresponding data output pins of the first group of data output pins; and the second group of output signals are provided to the corresponding data output pins of a second group of data output pins."

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven D. Radosevich whose telephone number is 571-272-2745. The examiner can normally be reached on 9am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jacques H. Louis can be reached on 571-272-6962. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Steven D. Radosevich
Examiner
Art Unit 2117

SHELLY CHASE
PRIMARY EXAMINER